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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Cecchi, et al.

Serial No.: (FILED CONCURRENTLY)

Filed: July 11, 2001

For: **CMOS Low Voltage High-Speed
Differential Amplifier**

Group Art Unit: (not yet assigned)

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

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July 11, 2001

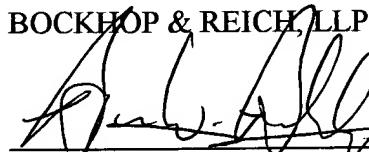
Sir:

Submitted herewith on form PTO 1449 is a listing of documents known to applicants and/or their attorneys in compliance with the requirements of 37 C.F.R. § 1.56. Copies of these documents are enclosed.

Consideration of the cited documents and making the same of record in the prosecution of the above-noted application are respectfully requested.

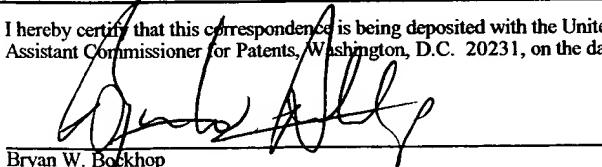
Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Assistant Commissioner for Patents, Washington, D.C. 20231, on the date written below.


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7/11/01